

# Electronics 1. Kirchhoff's laws, Thévenin's and Norton's theorems

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Week 2

# Kirchhoff's Current Law

## Kirchhoff's Current Law

The algebraic sum of currents entering and exiting a node equals zero

Convention (quite arbitrary): currents going into the nodes are positive, the ones which go out of the node are negative.

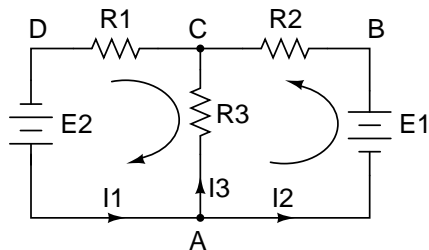
## Kirchhoff's Voltage Law

The algebraic sum of all voltage changes (aka voltage drops) in a loop equals zero

Notes:

- chose a direction along which you travel a network. If you go over a resistor and current runs the same way then voltage change is negative, otherwise its positive.
- If you go over a voltage source from negative terminal to positive the voltage change is positive, otherwise negative.

# Example



**our goal is to find  $I_1$ ,  $I_2$ , and  $I_3$**

We chose  $V_A = 0$

For node A:

$$I_1 - I_2 - I_3 = 0 \quad (1)$$

We need 2 more **independent** equations.

For this we will go over 2 small loops as indicated by arrows.

$$V_{DC} + V_{CA} + V_{AD} = 0 \quad (2)$$

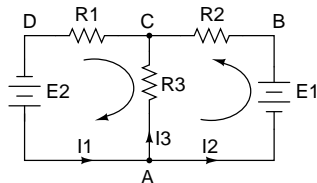
$$V_{AB} + V_{BC} + V_{CA} = 0 \quad (3)$$

Notice:

$$V_{AB} = +E1, \quad V_{BC} = -R2 \times I_2, \quad V_{CA} = +R3 \times I_3,$$

$$V_{DC} = +R1 \times I_1, \quad V_{AD} = -E2.$$

## Example (continued)



$$I_1 - I_2 - I_3 = 0$$

$$V_{DC} + V_{CA} + V_{AD} = 0$$

$$V_{AB} + V_{BC} + V_{CA} = 0$$

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$$I_1 - I_2 - I_3 = 0$$

$$R_1 \times I_1 + R_3 \times I_3 - E_2 = 0$$

$$E_1 - R_2 \times I_2 + R_3 \times I_3 = 0$$

# Maple as the math aid

`solve( {I1 - I2 - I3=0, E1 - R2·I2 + R3·I3=0, R1·I1 + R3·I3 - E2=0}, [I1, I2, I3])`

$$\left[ \left[ I1 = \frac{R3 E1 + R3 E2 + R2 E2}{R3 R1 + R1 R2 + R3 R2}, I2 = \frac{R3 E1 + R3 E2 + R1 E1}{R3 R1 + R1 R2 + R3 R2}, I3 = -\frac{R1 E1 - R2 E2}{R3 R1 + R1 R2 + R3 R2} \right] \right]$$

(1)

# Maple as the math aid (continued)

$$V_{out} := \frac{V_{in} \cdot R2}{(R1 + R2)} \cdot \frac{R_L}{R_L + \frac{R1 \cdot R2}{R1 + R2}}$$
$$\frac{V_{in} R2 R_L}{(R1 + R2) \left( R_L + \frac{R1 R2}{R1 + R2} \right)} \quad (1)$$

$$V_{in} := 10; R1 := 10; R2 := 1;$$

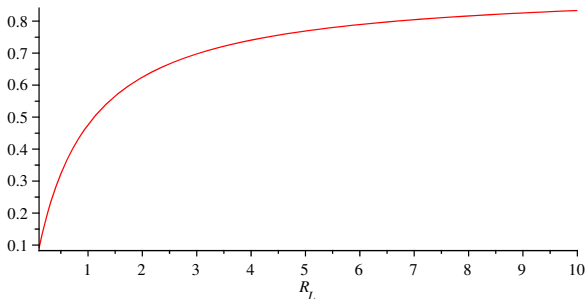
10

10

1

(2)

$\text{plot}(V_{out}, R_L = .1 \dots 10)$

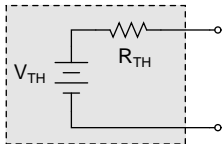


# Thévenin's and Norton's equivalent circuit theorems

Any combination of voltage sources, current sources and resistors with two terminals is electrically equivalent

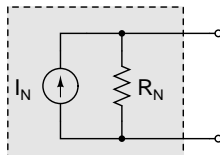
## Thévenin's theorem

to a single voltage source  $V_{TH}$  and a single series resistor  $R_{TH}$  connected in series.



## Norton's theorem

to a single current source  $I_N$  and a single series resistor  $R_N$  connected in parallel.



Note above circuits are equivalent to each other when

$$R_{TH} = R_N \text{ and } I_N = V_{TH}/R_{TH}$$