Physics 252 - Electronics I: Introduction to Analog Circuits.

Instructor: Eugeniy E. Mikhailov.

- Office: Millington 251
- Phones: 221-3571 (office), 251-3560 (lab)
- Email: eemikh@wm.edu
- Web: http://physics.wm.edu/~evmik/
- Office Hours:
  - M 14:05-15:00
  - T 9:00-10:00
  - W 9:00-10:00
  - and by appointment.

T.A. log book grader: Austin Ziltz.

- Email: arziltz@email.wm.edu

Course Objectives. The primary purpose of this course is to teach you how to design basic analog electronic circuits for connecting one device to another properly and efficiently: this is generally the function of most lab-built electronic circuits.

Along the way, we will also learn how to do basic analog circuit design and to a lesser extent signal acquisition and detection. More specifically, you will learn about the following essential concepts:

- impedance
- amplification
- frequency analysis
- feedback

You will also learn to use the following components and equipment:

- resistors, capacitors, inductors.
- diodes, photo-diodes, transistors, FETs.
- Op-amps, comparators.
- Multimeters, oscilloscopes, function generators.
- Breadboards and soldering irons.
- Modern circuit design and lay-out software.

Texts. There is no official textbook for the course. I will be posting introductory chapters and laboratories created specifically for the course on my website before the lecture. These chapters and labs were originally created by Prof. Jeff Nelson, Prof. Bill Cooke and Prof. Seth Aubin. They have been adapted to the current course. While there are many good electronics textbooks, I would use the following two:

- as a good introductory book "Basic Electronics: An Introduction to Electronics for Science Students" by Curtis. A. Meyer
  - available at print on demand web service lulu.com (direct link)
    - **Note**: This book has several typos and errors, please visit the Erratum Page
- as a **fantastic** reference book for design tips and concepts "The Art of Electronics" (2nd Edition, 1989-1999) by P. Horowitz and W. Hill.
  - It is available in the Library under call number TK7815.H67 1989. A copy is also available for reading in my office.

Both books will be useful for the Digital Electronics class in the next semester.

Class Format. The class hours are divided into two parts: Lecture and Lab. The lecture will be on Monday 1:00-1:50 pm in the Millington 117, and will cover the concepts to be tested in the lab later in the week. The lab portion of the class will be held on Wednesday 2:00-4:50 pm for Physics 252-01 and Thursday for Physics 252-02 in the Millington 207.

Evaluations. Your final grade for the course will be determined from the following grading weight distribution:

- Logbooks: 40% (design exercises 15%, lab 25%)
- Quizzes: 10%
- Participation: 5%
- Midterm: 20%
- Final: 25%

Lab books. Your lab book should be a regular style notebook without rings with either line or quadrangle ruling or a computation logbook. It can be obtained at most stationary stores (i.e. Staples, etc ...)

Your lab book is the primary record of your work and data. You should record everything that you do in the lab book, so that anyone (such as the instructors and yourself) can understand what you have done and measured. You should include circuit diagrams, observations, questions, answers, design considerations, measurement data, and analysis. Diagrams, data, graphs, and other notes on separate pieces of paper should be glued, taped, or stapled into the lab book. If something falls out of the lab book during reading/shaking/transporting, it is not the part of the log book and will be discarded. All notes should be written in **pen**. Mistakes and errors in design, data, and analysis will occur, and they should be crossed out neatly.

The lab book will be graded primarily on completeness and to a lesser extent on neatness (i.e. better to be complete than neat, though doing both is better yet). It should also feature a table of contents. The lab books will be turned in every week or two and returned before the next lab.

You should enter you lab notes and data directly into the lab book. A "scratch" lab book that is neatly copied into the lab book at a later time is not appropriate and will result in a significantly reduced participation grade. It is OK though to use scratch paper which is glued into the lab book after.

Design Exercises. Most labs will include a design component. **The design exercises must be prepared prior to attending lab**. Their main goal is to prepare you for the labs. I will check preparation of the design exercise at the beginning of each lab. **An unprepared/incomplete design exercise will have up to 50% penalty.**

Quizzes. There will be 15 minutes quizzes at the beginning of a lab to encourage you to review concepts and circuit design.

Grading.

Grade	Score percentage	Grade	Score percentage	Grade	Score percentage
		A	94-100	A-	90-93
B+	87-89	B	84-86	B-	80-83
C+	77-79	C	74-76	C-	70-73
D+	67-69	D	64-66	D-	60-63
F	<60				

Midterm test. There will a 1 hour midterm test in lab on February 23-24. There will be a lab session after the midterm.

Final exam. There will be a final exam on Thursday May 5 (2:00pm - 5:00pm) covering all course materials.

Due dates/time. Lab books are due by 5pm on next day after lab (i.e. Thursdays for the Wednesday section and Fridays for the Thursday section) and will be returned by the next lecture.

Late logbook submissions will have points deducted. If you know you will have a problem getting the report on time please send me an email as soon as you can to let me know about your situation.

Illness. Please notify the instructor if you are ill, so that arrangements can be made to make up missed labs.

Weekly Topics.

Week 0: 1/17	NO CLASS
Week 1: 1/24	DC Circuits Basics.
Week 2: 1/31	Kirchhoff's Law's and Thevenin's Theorem.
Week 3: 2/07	Capacitors, Inductors, and Complex Impedance.
Week 4: 2/14	Passive Filters and Transmission Lines
Week 5: 2/21	Diodes. **MIDTERM TEST** during labs time
Week 6: 2/28	Transistors 1: BJTs
Spring Break	NO CLASS
Week 7: 3/14	Transistors 2: More BJTs
Week 8: 3/21	Transistors 3: FETs
Week 9: 3/28	Op-Amps 1: Introduction to Op-Amps
Week 10: 4/4	Op-Amps 2: detectors, filters, power amplifiers
Week 11: 4/11	PID Control Theory
Week 12: 4/18	Electronic Circuit Design Tools
Week 13: 4/25	Comparators
Week 14: 5/05	**FINAL EXAM** Thursday May 5, 14:00-17:00